

FIG. 1

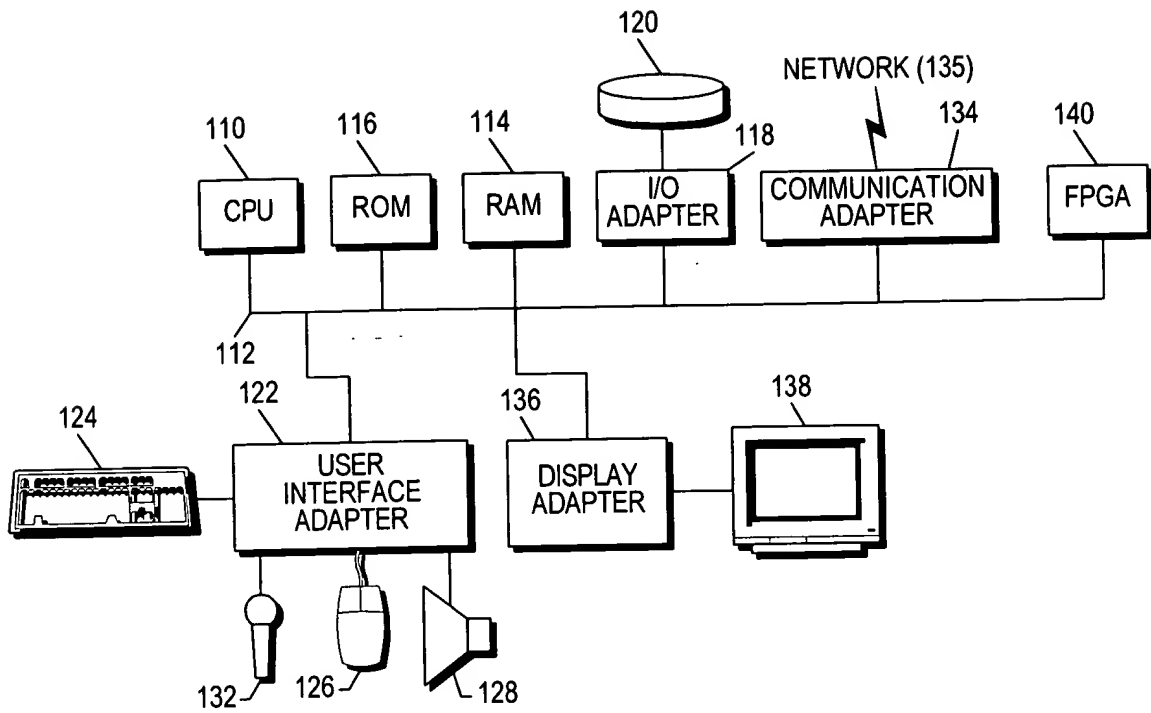


Fig. 1

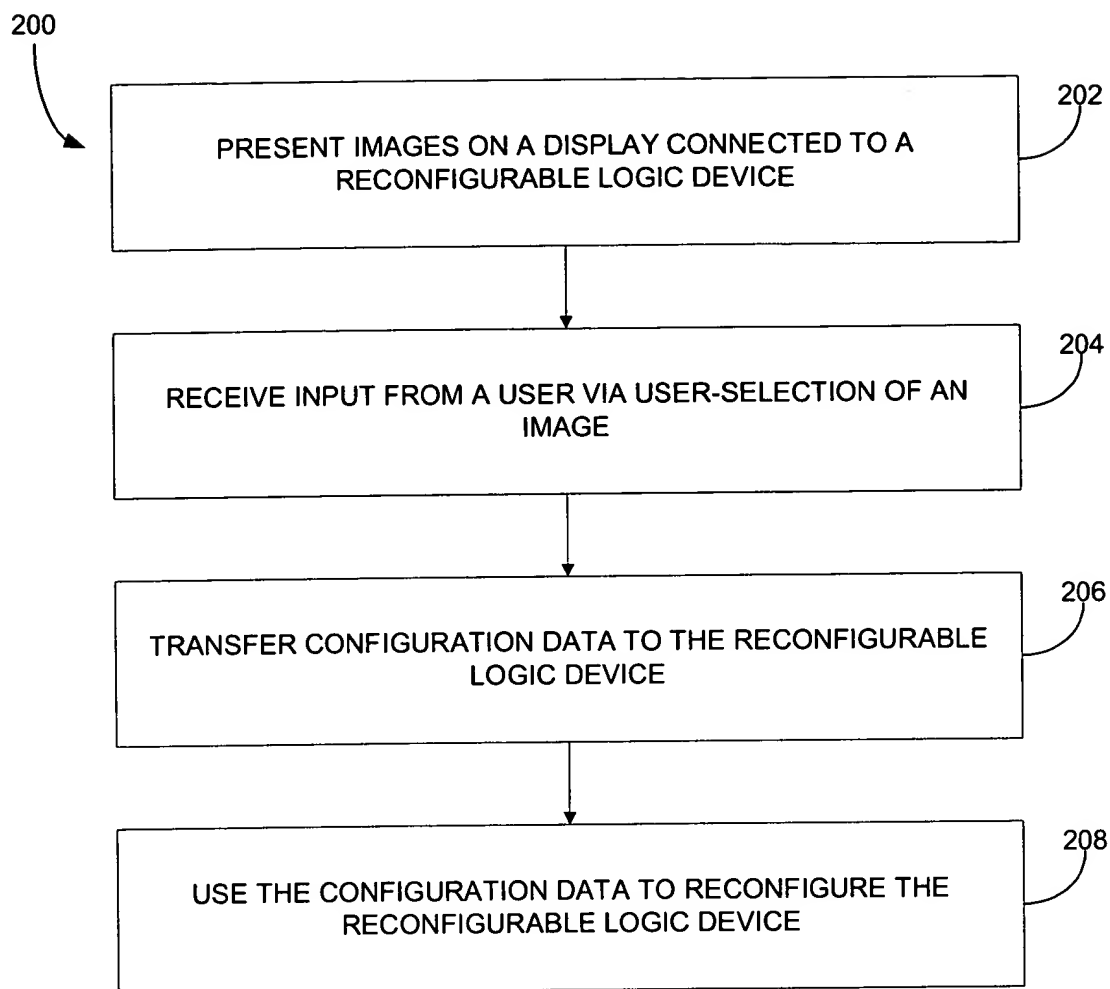


Fig. 2

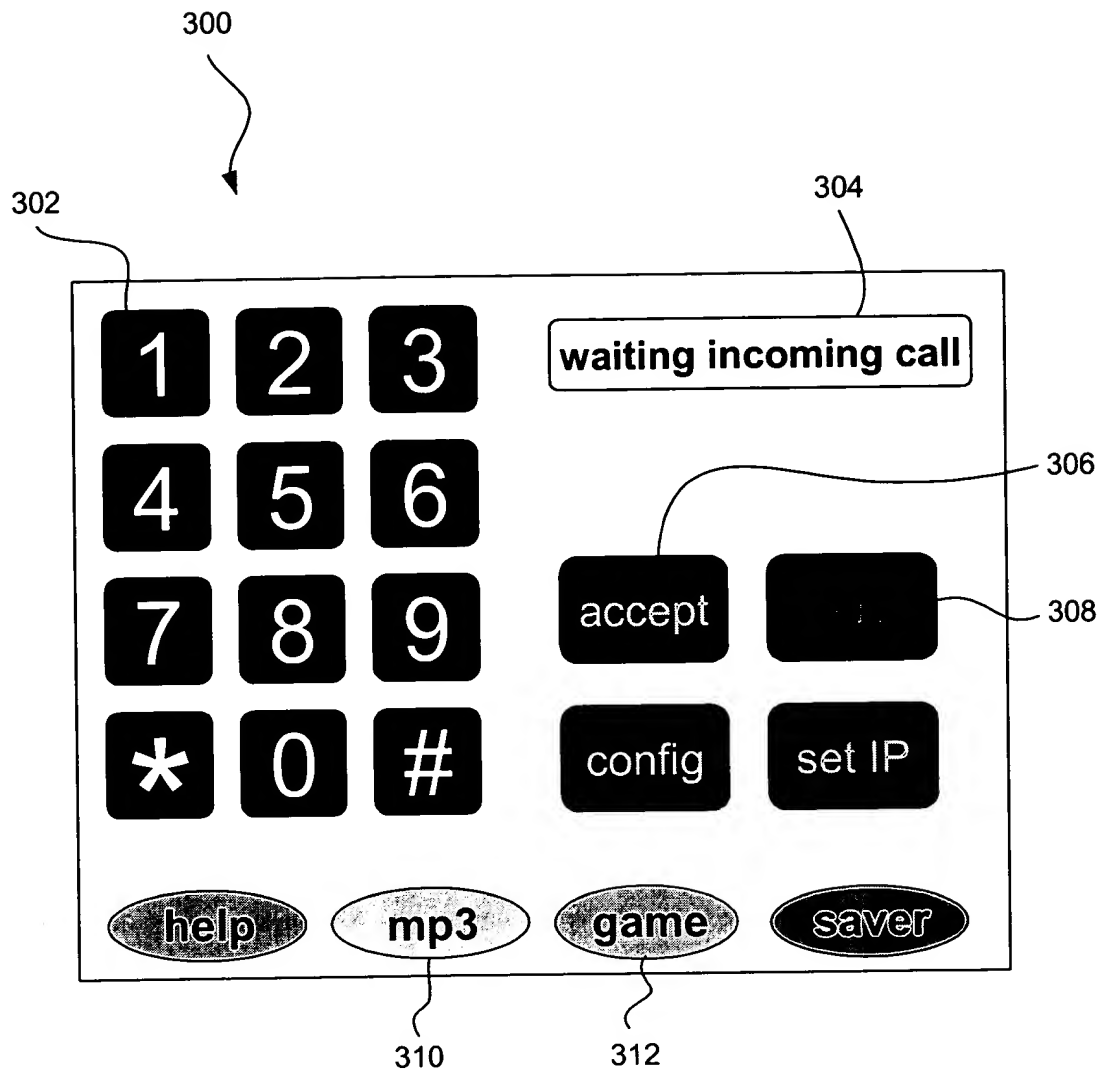


Fig. 3

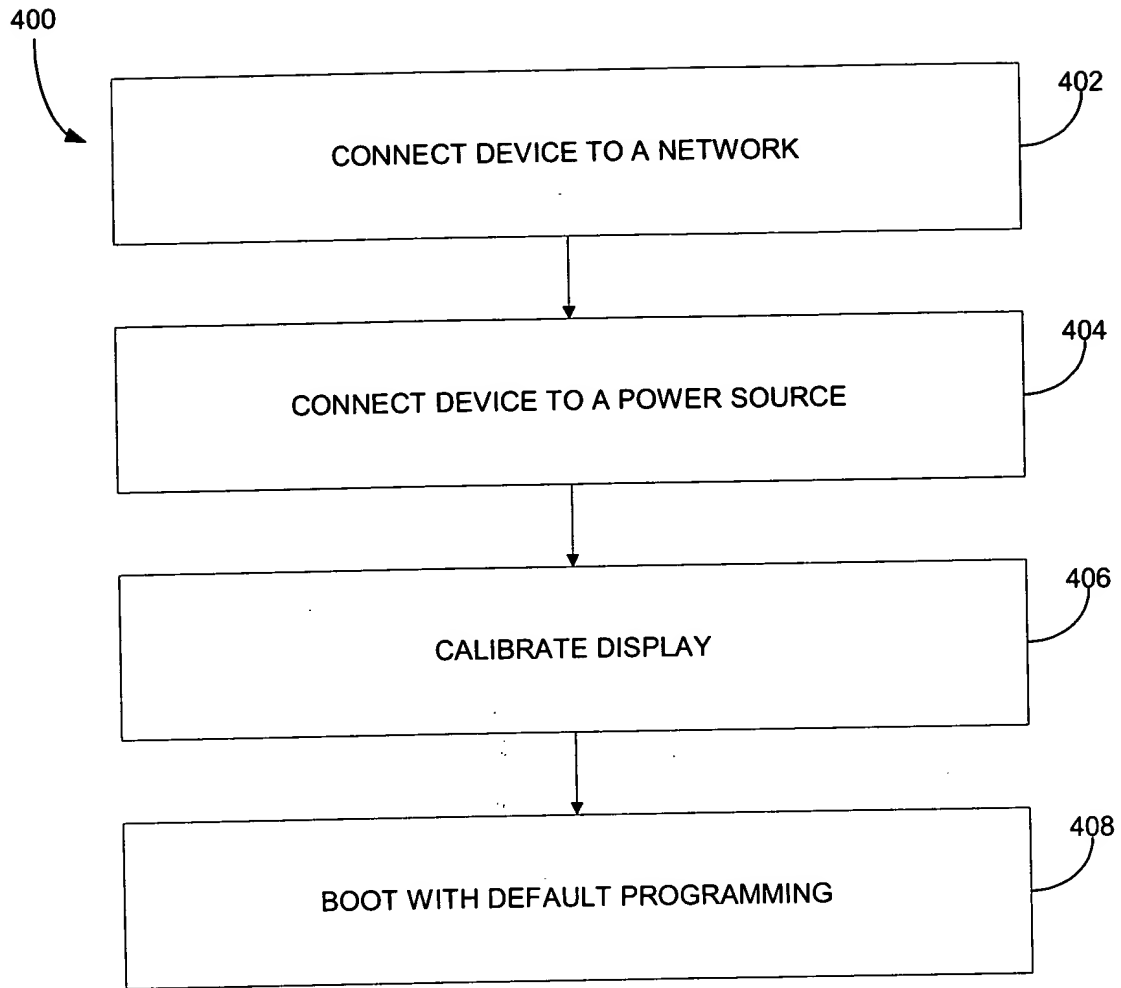


Fig. 4

FIG. 5

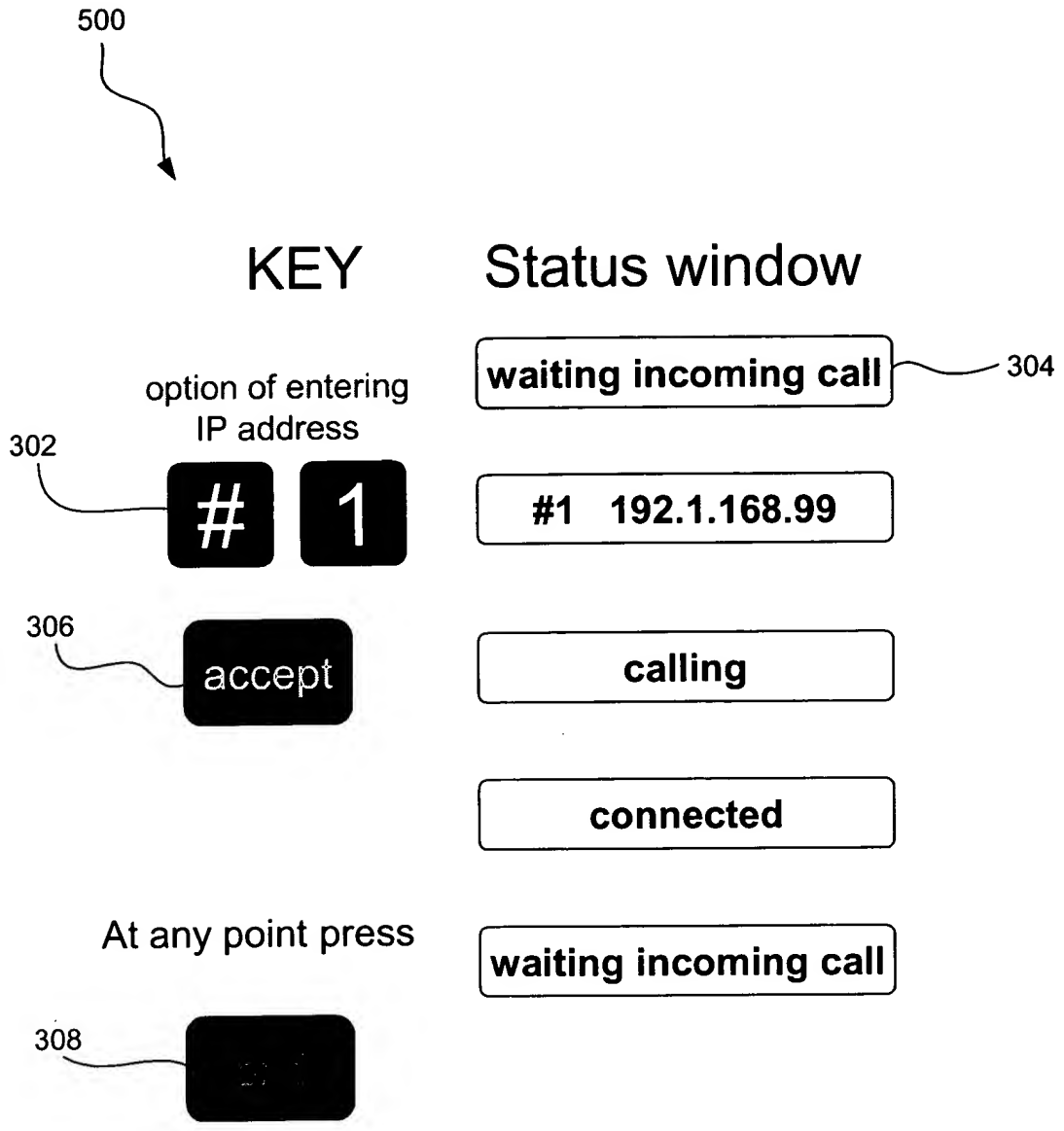


Fig. 5

FIG. 6

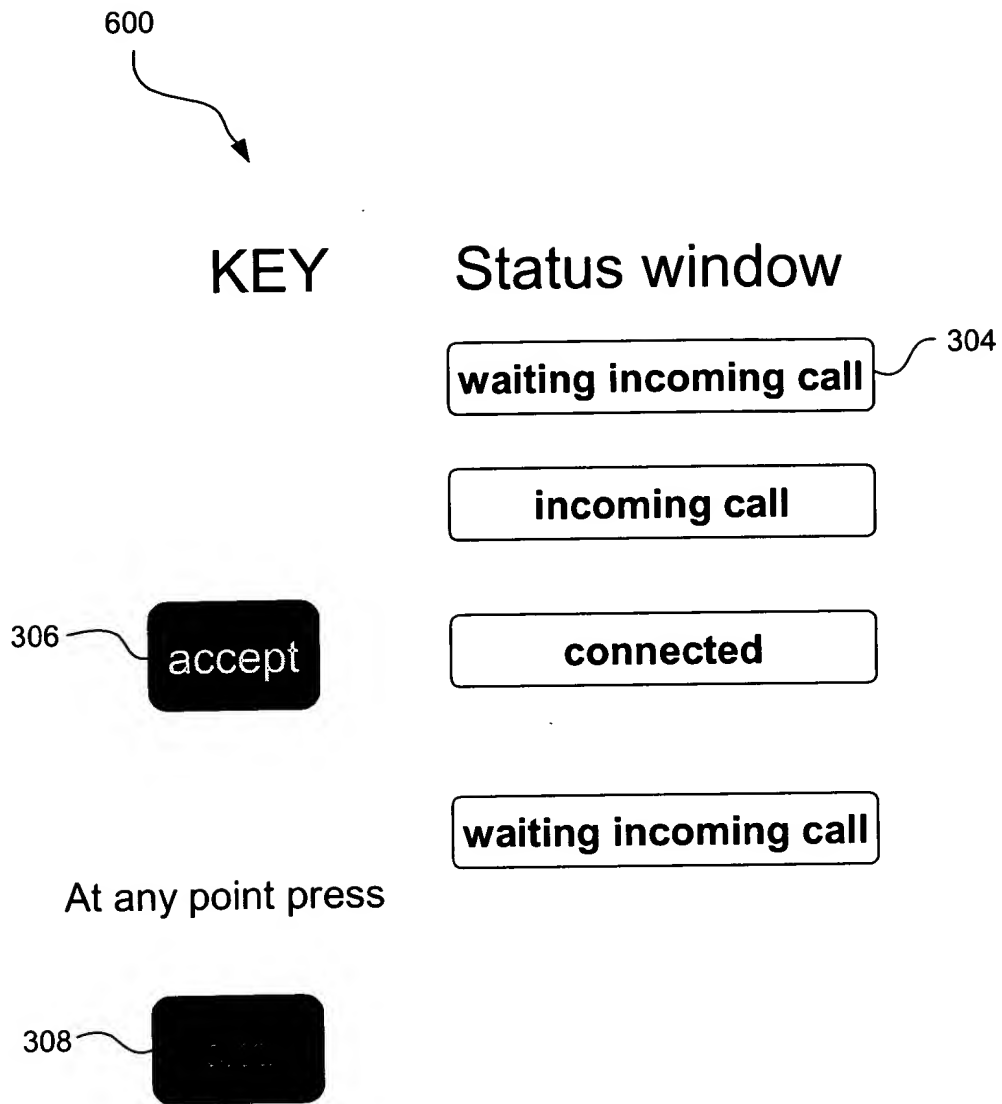


Fig. 6

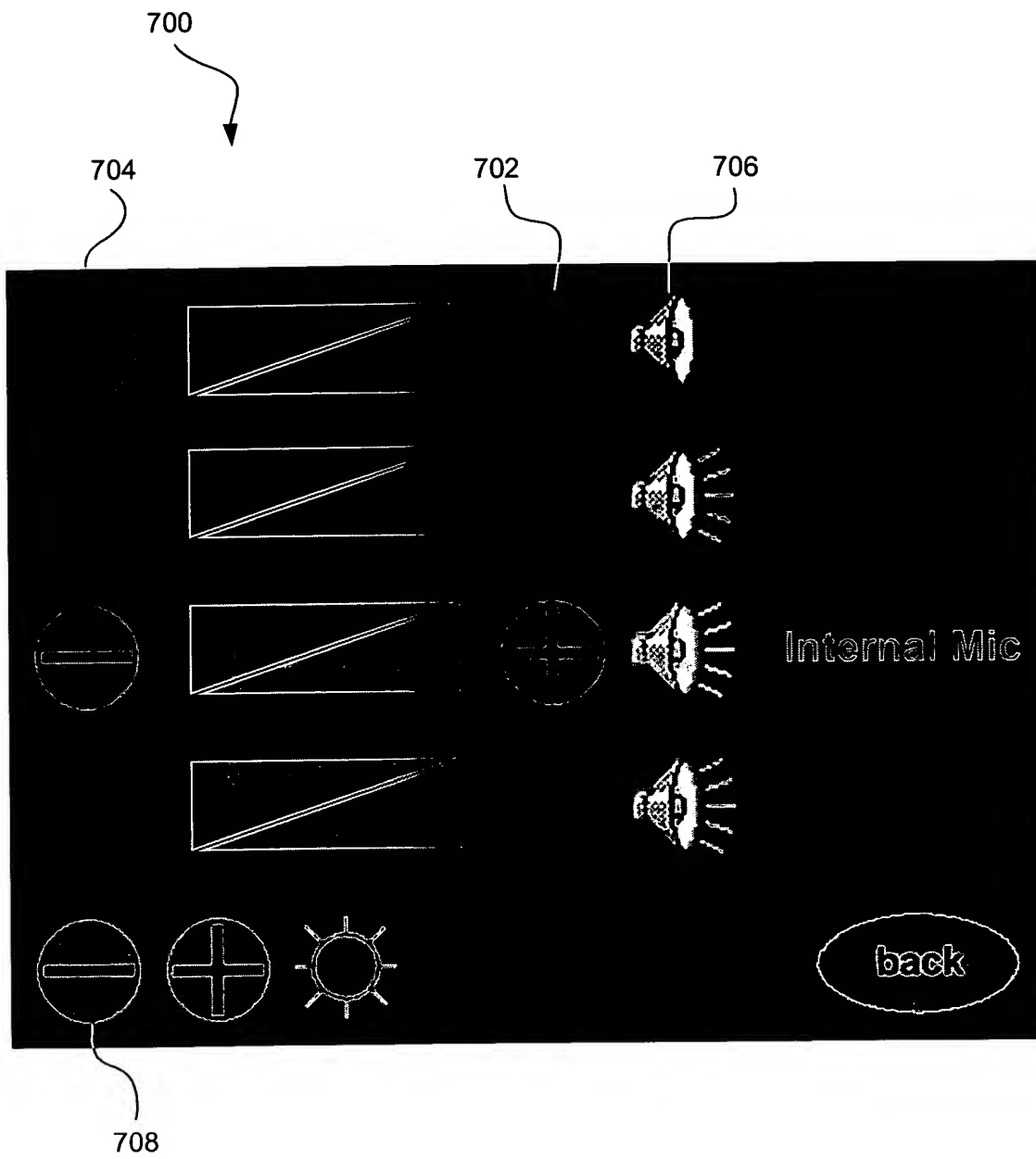


Fig. 7

09772534.012901
T062T0"4E52Z60

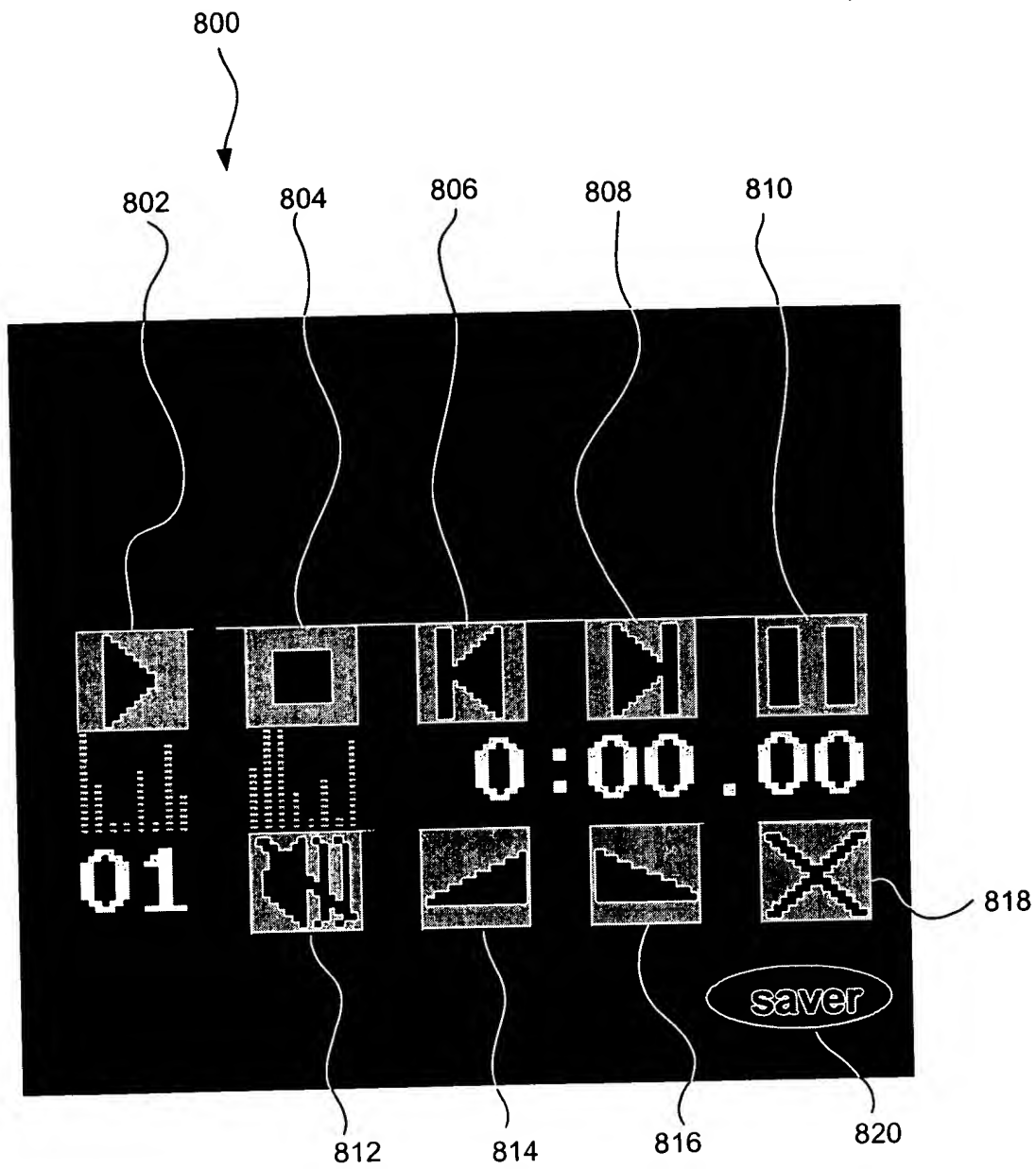


Fig. 8A

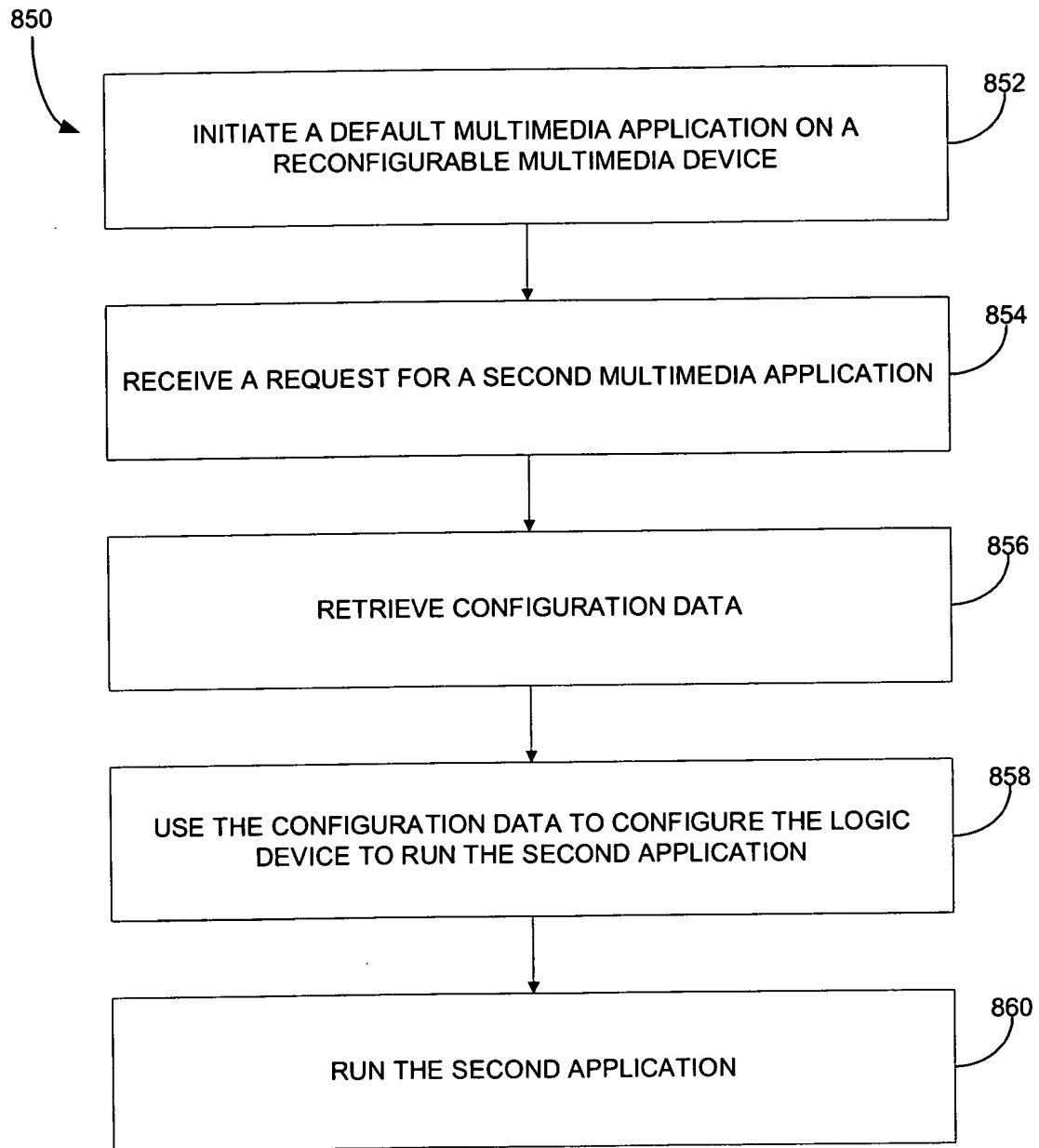


Fig. 8B

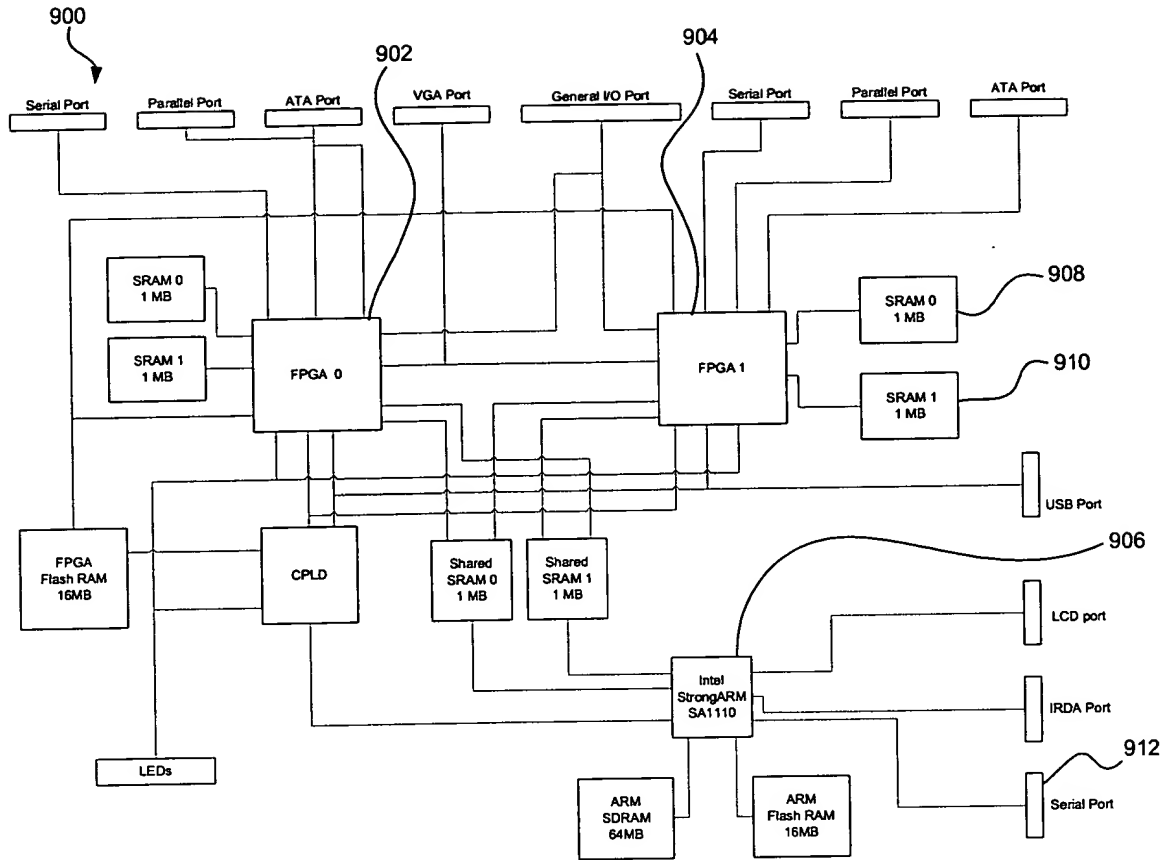


Fig. 9

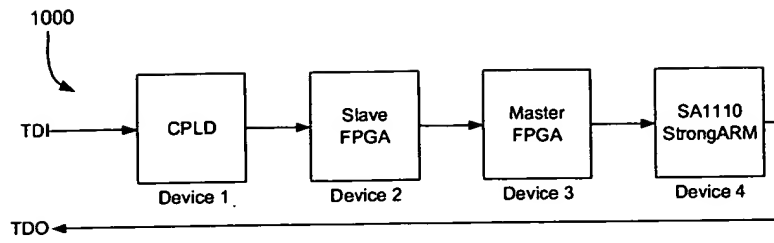


Fig. 10

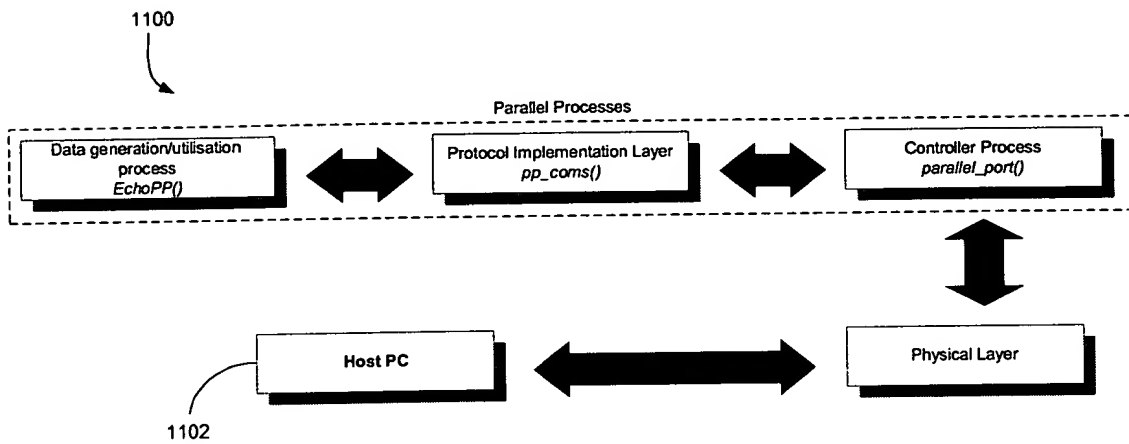


Fig. 11

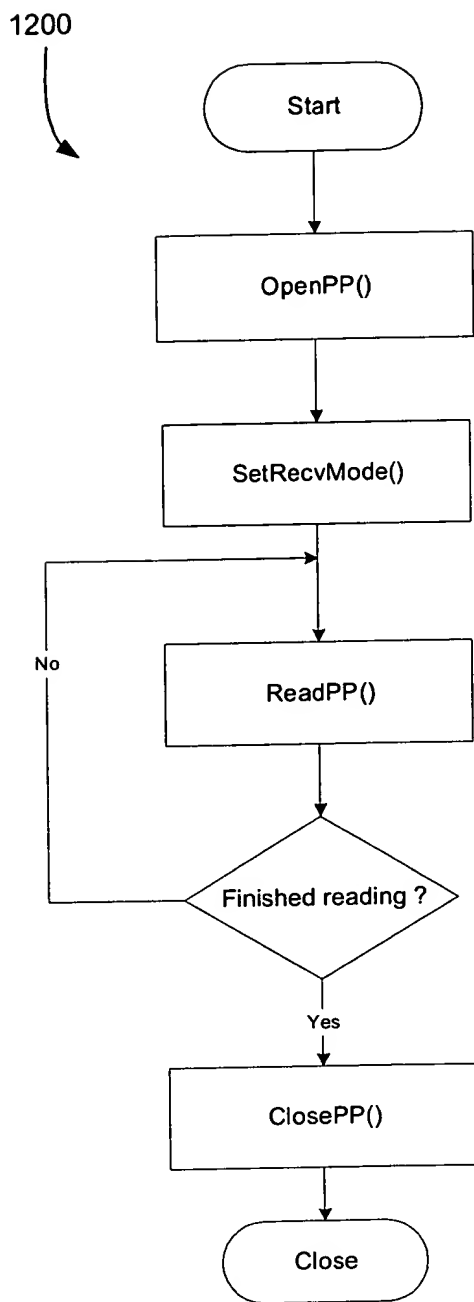


Fig. 12

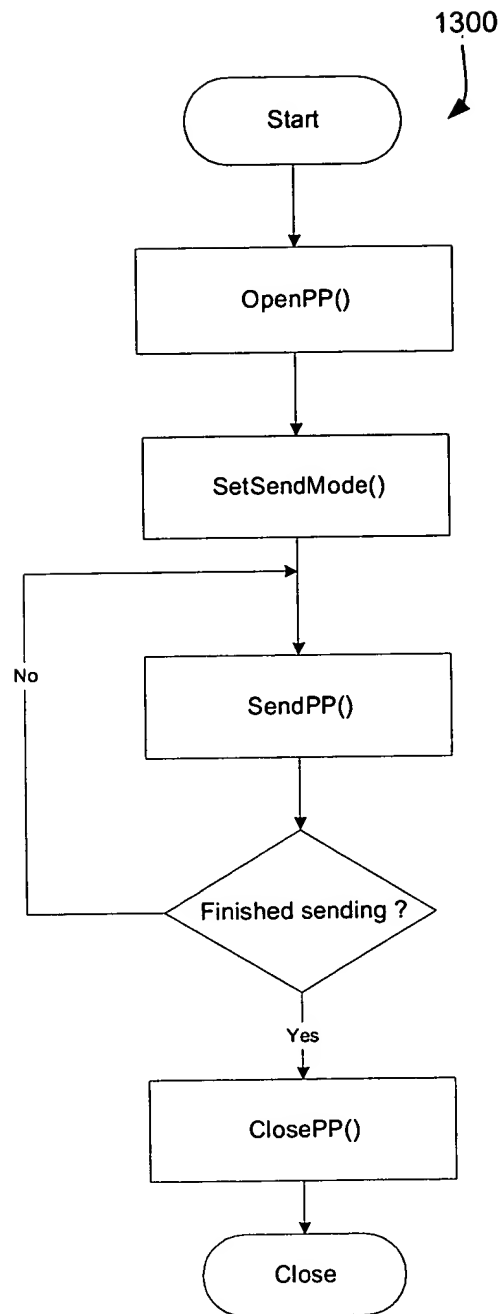


Fig. 13

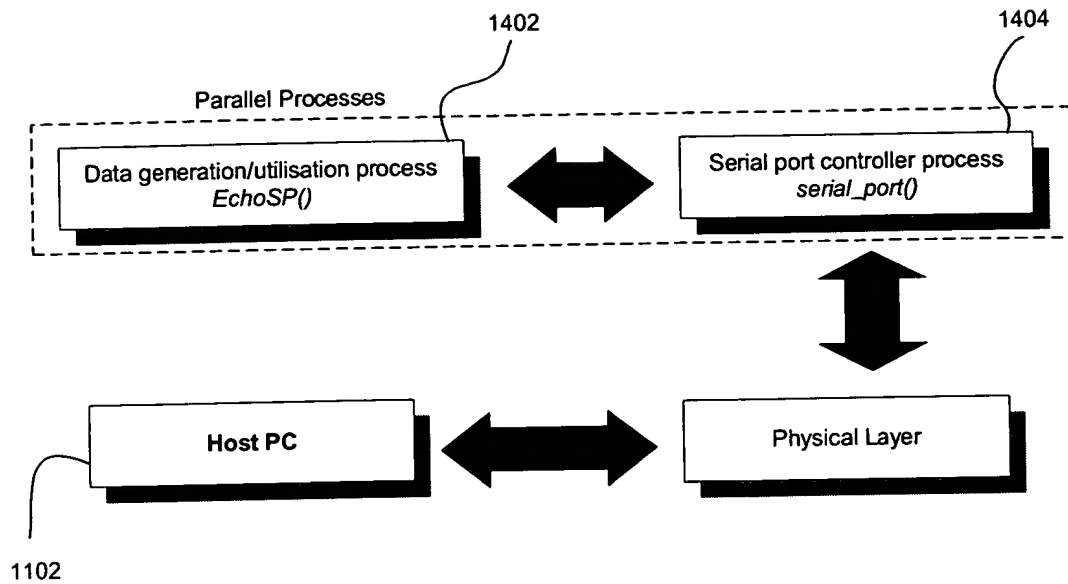


Fig. 14

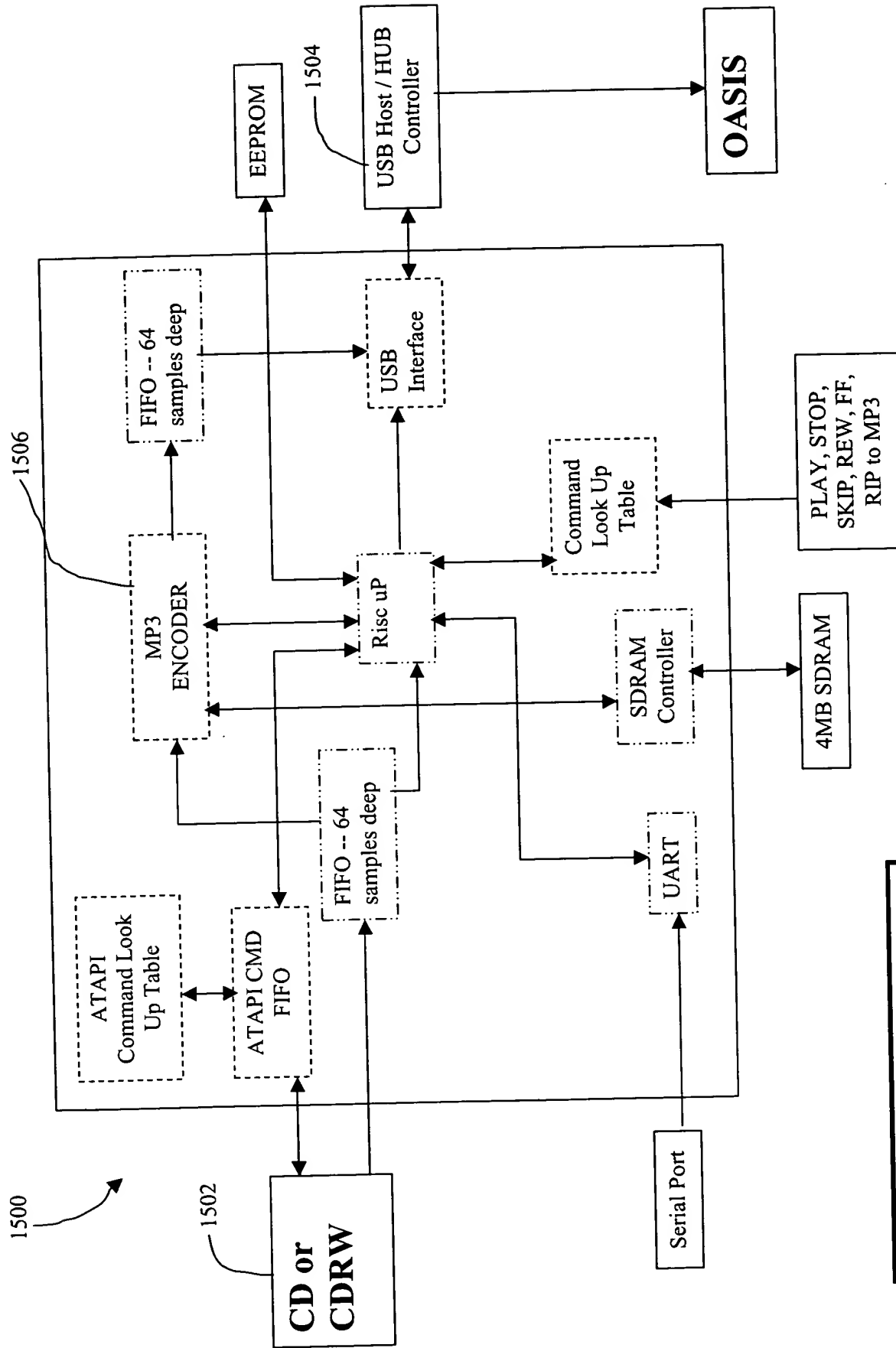


Fig. 15

LEGEND:
 Off the shelf components
 Logic
 IP Core from Xilinx

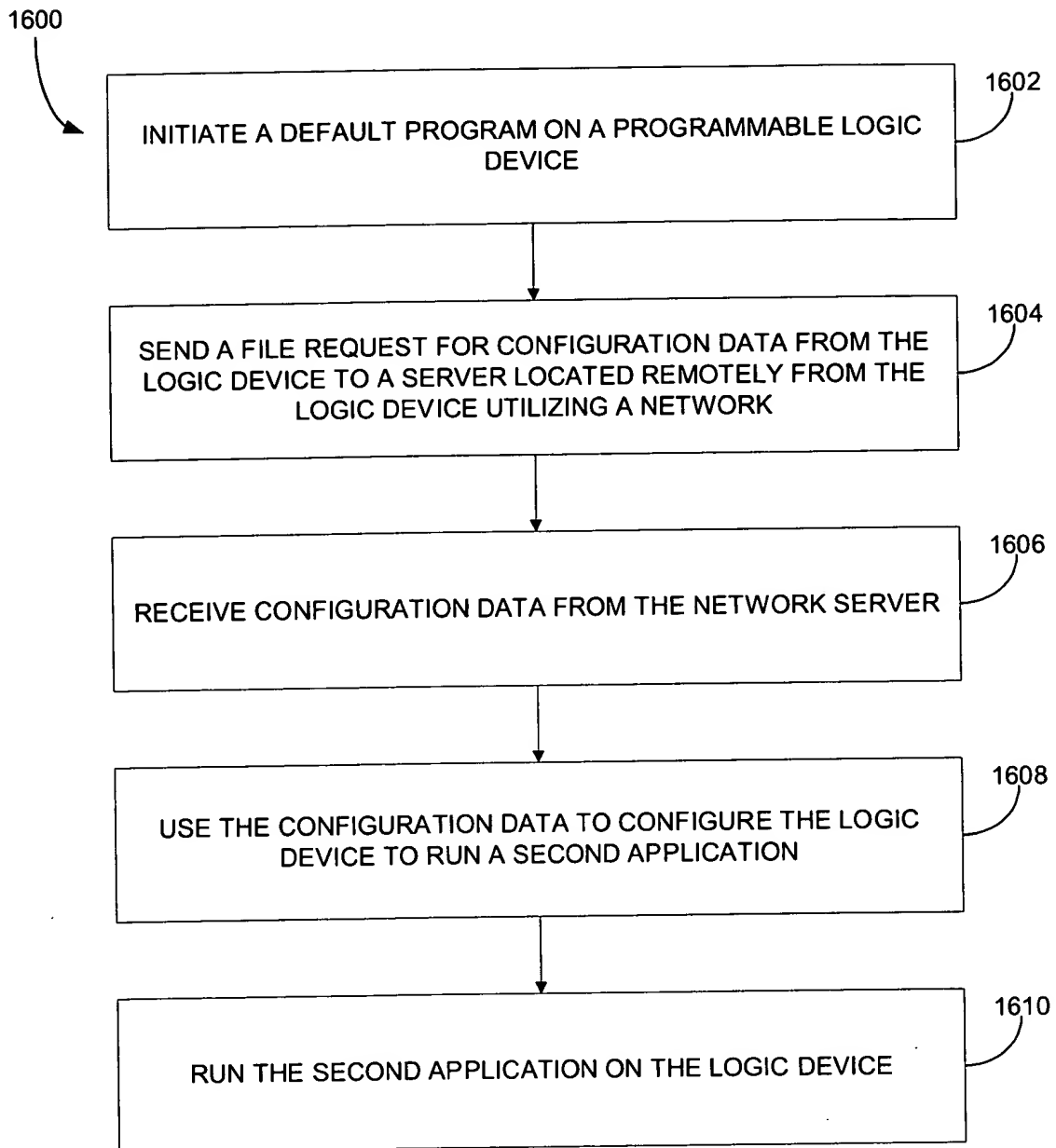


Fig. 16

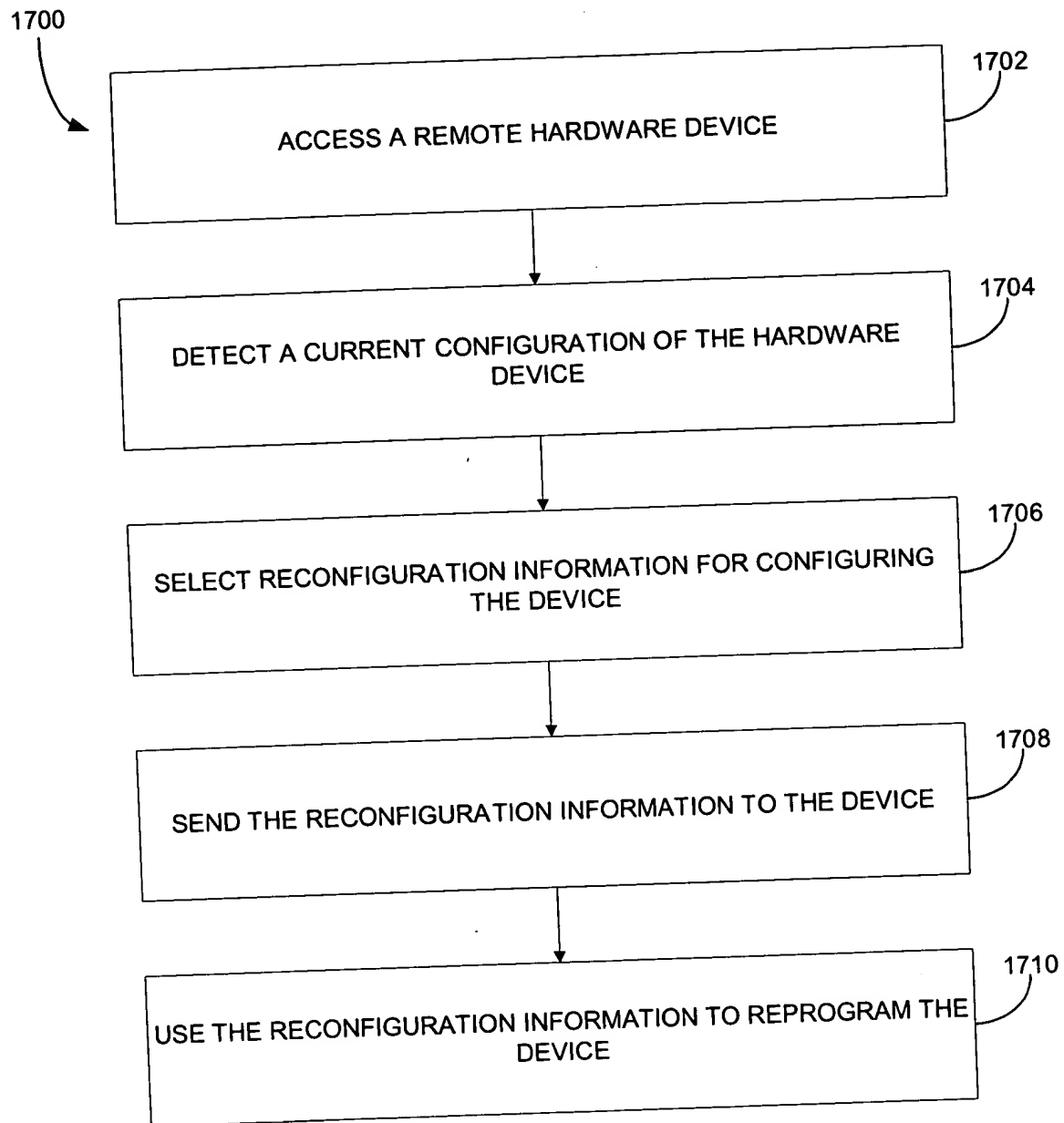


Fig. 17

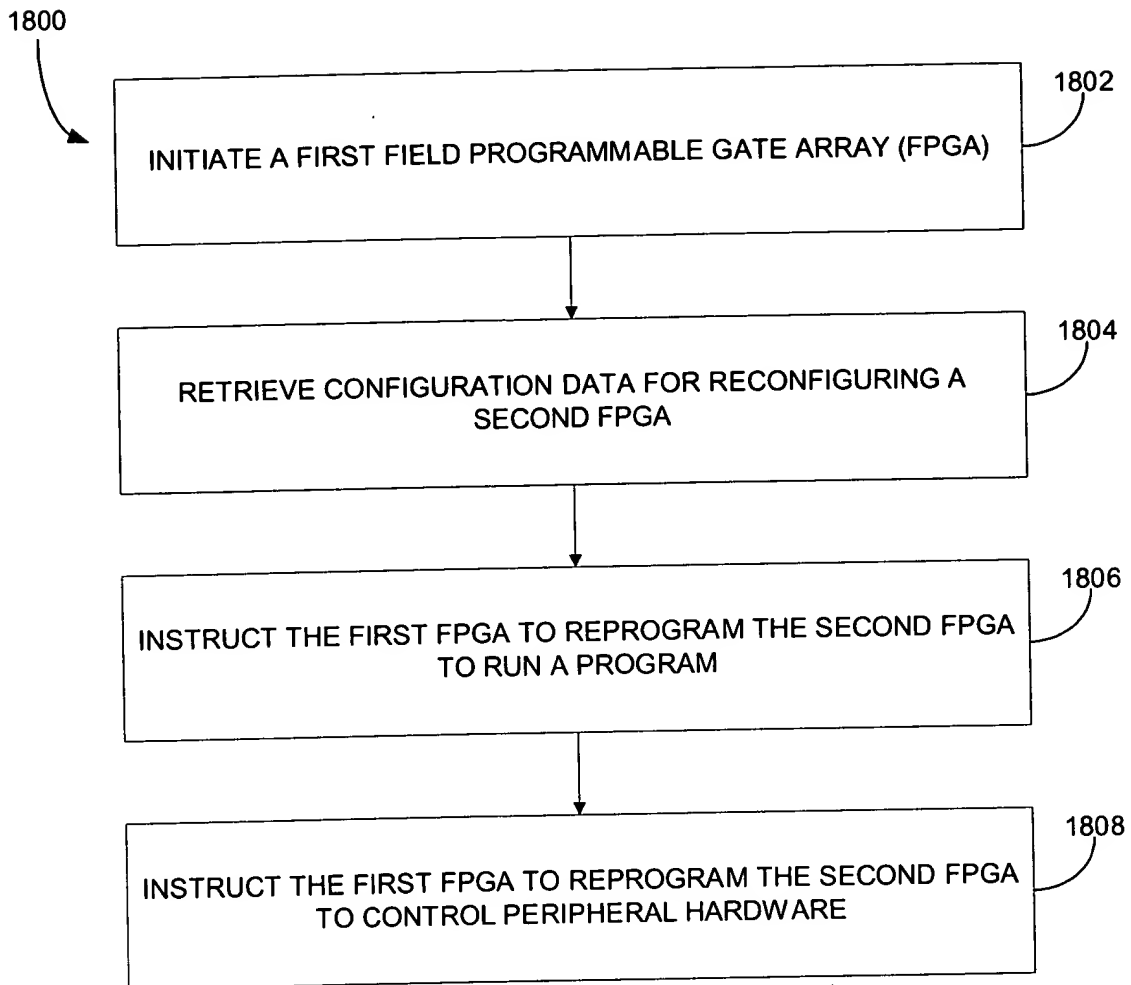


Fig. 18